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|-----------------------------------|--|-------------------------|---|-------------|
| <b>Notice of References Cited</b> |  | Application/Control No. | Applicant(s)/Patent Under Reexamination |             |
|                                   |  | 10/688,573              | ZEIDMAN, ROBERT M.                      |             |
| Examiner                          |  | Art Unit                |   | Page 1 of 1 |
| BEN C. WANG                       |  | 2192                    |   |             |

**U.S. PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

|   |   |  |
|---|---|--|
| * |   | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)  |
|   | U | Stewart et al., "The Chimera Methodology: Designing Dynamically Reconfigurable Real-Time Software using Port-Based Objects", 1995, IEEE, pp. 46-53 |
|   | V |  |
|   | W |  |
|   | X |  |

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